

Amendments to the Claims

1. (original) A programmable clock generator for providing multiple clock signals with non-overlapping edges, comprising:
 - means for receiving an external signal from an external clock and providing first and second clock signals as a result of said external signal;
 - first programmable delay means, coupled to said receiving means, for delaying said first clock signal and feeding a resultant delayed first clock signal back into said receiving means to change the phase of said second clock signal based on the amount of delay introduced by said first programmable delay means; and
 - second programmable delay means coupled to said receiving means, for delaying said second clock signal and feeding a resultant delayed second clock signal back into said receiving means to change the phase of said first clock signal based on the amount of delay introduced by said second programmable delay means.

Claims 2-19 (canceled)

This listing of claims will replace all prior versions, and listings of claims in the application.